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Auburn University, Auburn, AL 36849 \* School of ECE, Georgia Tech

SRC Task 1133.001 (2003-2006)



## Task Objectives

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- Development of more accurate RF noise models for SiGe RFIC design
  - Methodologies of RF noise source extraction
  - Evaluation of existing models
  - New model and parameter extraction development
  - Model verification on 50 / 120 / 200 GHz HBTs for SRC member companies
- Tools for application of new model in circuit design
  - Matlab codes for model parameter extraction
  - Verilog-A based models (VBIC based) for application of the new model in circuit design

## Personnel

#### · PI and Co-PI

- \* Guofu Niu (Auburn) and John Cressler (Georgia Tech)
- Students
  - Kejun Xia (Auburn, PhD, to graduate Fall 2005, presenter)
     Qingqing Llang (Georgia Tech, PhD, graduated Spring 2005,
  - Qringqing Liang (Georgia Tech, PhD, graduated Spring 2005, now at IBM)
- Industrial Liaisons
  - David Sheridan (IBM)
     Sheridan (IBM)
  - Shaikh F. Shams and Hernan A. Rueda (Freescale)

#### Industry Interaction and Knowledge Transfer

- Extensive collaboration and transfer of research result to IBM and Freescale
  - New model
  - Parameter extraction methods
- The PI visited Freescale in Dec 2004
  - · Presentation of results
  - Transfer and demonstration of noise extraction and modeling matlab codes
- Numerous interactions with IBM Modeling group
- Student internship
  - · Summer 2004 at IBM, Burlington with Scott Parker

# Past Year Accomplishments

- Noise source extraction / parameter extraction methods
- Major models evaluated
- Connections between different models established
- A new expression for noise crowding effect derived
- Noise crowding effect quantified experimentally
- A new model for all noise sources is developed
  - Explicit modeling of frequency dependence through w or w^2
  - Explicit modeling of current dependence through gm
  - Scalable over multiple geometries
  - Extensive verification using measured and simulated data on 50 GHz HBTs
  - Initial investigation on 200 GHz HBTs

#### Publications

- G.F. Niu, K. Xia, D. Sheridan and S. Sweeney, "RF Noise Modeling in SiGe HBTs," International Conference on Noise and Fluctuations . 2005 (Invited talk).
- (ICNF), Spain. 2005 (Invited tark).
  G.F. Niu, "Noise In SiGe HBT RF Technology: Physics, Modeling and Circuit Implications," Review Paper, Special Issue of Proceedings of the IEEE on SiGe Technology, to appear, 2005
  G.F. Niu, K. Xia, D. Sheridan and S. Sweeney, "Physics and Modeling of RF Noise In SiGe HBTs," invited talk, Workshop on Compact Modeling, May 2005.
  C.E. Niu, K. Ya, D. Sheridan, D. Harame, "Experimental Extraction

- Modeling, May 2005. G. F. Niu, K. Xia, D. Sheridan, D. Harame, "Experimental Extraction and Model Evaluation of Base and Collector Current RF Noise in SiGe HBTs," *Tech. Digest of IEEE Radio* Frequency Integrated Circuits Symposium (RFIC), pp. 615-618, June 2004. K. Xia and G.F. Niu, "Ratio based small signal parameter extraction of SiGe HBTs," *Proc. IEEE BCTM*, pp. 144–147, Sep. 2004. G.F. Niu, "Bridging the Gap Between Microscopic and Macroscopic Theories of Noise in Bipolar Junction Transistors," *IEEE Topical Meeting on Si Monolithic Integrated Circuits in RF Systems*, pp. 227 230, 2004.

#### Publication cont.

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K. Xia, G. Niu, D. Sheridan and S. Sweeney, Frequency and bias dependent modeling of correlated base and collector current RF noise in SiGe BTs", under review, IEEE Trans. Electrons Devices.

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K.Xia, G. Niu, D. Sheridan and S. Sweeney, "Input nonquasi Static effect in SiGe HBTs and its impact on Noise Modeling", accepted, IEEE BCTM 2005.

#### JRC Noise Figure: Definition and Importance

- Noise Figure (NF) = (Si/Ni)/(So/No) > 1 as amplifier adds noise
- NF determines Minimum Detectable Signal and hence sensitivity of a wireless system
- Cost and density of base station infrastructure are directly . determined by receiver noise figure
- 1dB degradation in Noise Figure is a big deal for cell phones, it means 26% more additional base stations



Minimization of noise through bandgap engineering as well as accurate modeling of noise are important!











New Semi-empirical Extraction Based	Jac
Intrinsic Noise Model	Pare

- · Pros:
  - Using conventional models without input NQS effect
  - Infrastructure of conventional models can be used
- · Cons:
  - Resulting noise currents may not be completely physical
- Involve several additional parameters dedicated to noise modeling
   Technical approach:
  - Small signal parameter extraction to obtain accurate and physically meaningful values
  - Intrinsic noise sources extraction using de-embedding of all circuit elements step by step using standard noise circuit analysis theory (Hillbrand and Russer)
  - Intrinsic noise sources modeling
  - Geometry scaling examination

































AE=0.24x20x2 um <sup>2</sup> (the reference transistor) Parameters are in MKS unit			
Parameter	Value	Parameter	Value
abb.	2	K	$1.0245 \times 10^{-43}$
ai	1.8	K'cbr	$4.6690 \times 10^{-32}$
acc	1	Kee	$1.6345 \times 10^{-20}$
B <sub>bb</sub> -	0	Biet	$1.0209 \times 10^{-34}$
K tr	$3.0369 \times 10^{-22}$	K kr	8.1213 × 10 <sup>-44</sup>
Beet	$8.2843 \times 10^{-21}$		

















