[X] ORIGINAL [ ] REVISION NO. _____

Project No. _A-2948_   DATE _10-7-81_

Project Director: _W. D. Spencer_   ~~School~~/Lab _SEL_

Sponsor: _Fairchild Camera and Instrument Corporation_

Type Agreement: _Purchase Order No. PL 32076_

Award Period: From _5-15-81_ To _12-15-81_ (Performance) _12-15-81_ (Reports)

Sponsor Amount: _$125,260_   Contracted through:

Cost Sharing: _____   GTRI/~~EES~~

Title: _MILSTD 1750A Microprocessor Logic Design and Simulation_

ADMINISTRATIVE DATA   OCA Contact _Don Hasty_

1) Sponsor Technical Contact: 2) Sponsor Admin/Contractual Matters:

X

All contacts:

Dr. Thomas A. Longo → LoNgo
Chief Technical Officer
Fairchild Camera and Instrument Corporation
4001 Miranda Avenue
Palo Alto, CA 94304

Defense Priority Rating: _N/A_   Security Classification: _N/A_
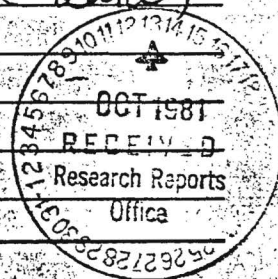

RESTRICTIONS

See Attached _____ Supplemental Information Sheet for Additional Requirements.

Travel: Foreign travel must have prior approval — Contact OCA in each case. Domestic travel requires sponsor

approval where total will exceed greater of $500 or 125% of approved proposal budget category.

Equipment: Title vests with _N/A – None proposed_

COMMENTS:

Close, tight restrictions on handling of all data, advertising, publicity, and inventions.

COPIES TO:

Administrative Coordinator   Research Security Services   EES Public Relations (2)
Research Property Management   ~~Reports Coordinator (OCA)~~   Computer Input
Accounting   Legal Services (OCA)   Project File
Procurement/EES Supply Services   Library   Other _____
FORM OCA 4:781

GEORGIA INSTITUTE OF TECHNOLOGY

OFFICE OF CONTRACT ADMINISTRATION

SPONSORED PROJECT TERMINATION/CLOSEOUT SHEET

Date _____1/24/84_____

Project No. __A-2948__   X̶S̶c̶h̶o̶o̶l̶X̶Lab ___SEL___

Includes Subproject No.(s) ___None___

Project Director(s) ___W. D. Spencer___   GTRI / G̶X̶X̶X

Sponsor ___Fairchild Camera and Instrument Corporation___

Title ___MILSTD 1750A Microprocessor Logic Design and Simulation___

Effective Completion Date: ____12/15/81____   (Performance) __12/15/81__ (Reports)

Grant/Contract Closeout Actions Remaining:

[X] **None**   FOR INFORMATION ONLY

[ ] Final Invoice or Final Fiscal Report

[ ] Closing Documents

[ ] Final Report of Inventions

[ ] Govt. Property Inventory & Related Certificate

[ ] Classified Material Certificate

[ ] Other _____

Continues Project No. _____   Continued by Project No. ___A-3108___

COPIES TO:

Project Director
Research Administrative Network
Research Property Management
Accounting
Procurement/EES Supply Services
Research Security Services
Reports Coordinator (OCA)
Legal Services

(Library)
GTRI
Research Communications (2)
Project File
Other _____

Form OCA 60:1028

15 October 1981

Fairchild Camera and Instrument Corporation
4001 Miranda Avenue, MS 30-110
Palo Alto, CA 94304

ATTENTION: Dr. Thomas A. Longo
              Contracting Officer

REFERENCE: PL 32076
              MILSTD 1750A Microprocessor
              Logic Design and Simulation

SUBJECT: Monthly Status Report No. 1

Gentlemen:

A summary of the progress on the referenced contract through 30 September is contained herein.

Objective

The purpose of this effort is to provide specialized engineering services and indirect materials in the support of architectural design, logic design, and software simulation portions of the F9450 microprocessor which will implement the MIL-STD-1750A Instruction Set.

Summary of Technical Efforts

Technical efforts are reported weekly be informal means. They have included such results as the following:

1. Developing Register Transfer Level (RTL) descriptions
2. Establishing microword field formats
3. Control store bit size compression
4. Initial high-level CPU simulations
5. Preliminary analysis of MMU structure.

Efforts Expended

	Man-Hours
Professional Personnel	620
Technical Support Personnel	130

Expenditures

Cumulative expenditures through the month of August 1981 were $56,944. Estimated expenditures during September 1981 were $35,130 leaving an estimated balance of $33,186 from the project ceiling price of $125,260.

Respectfully submitted,

W. Dean Spencer
Project Director

APPROVED:

Frederick B. Dyer, Chief
Defense Systems Division

9 November 1981

Fairchild Camera and Instrument Corporation
4001 Miranda Avenue, MS 30-110
Palo Alto, CA 94304

ATTENTION: Dr. Thomas A. Longo
                  Contracting Officer

REFERENCE: PL 32076
                  MILSTD 1750A Microprocessor
                  Logic Design and Simulation

SUBJECT: Monthly Status Report No. 2

Gentlemen:

A summary of the progress on the referenced contract through 30 October is contained herein.

Objective

The purpose of this effort is to provide specialized engineering services and indirect materials in the support of architectural design, logic design, and software simulation portions of the F9450 microprocessor which will implement the MIL-STD-1750A Instruction Set.

Summary of Technical Efforts

Technical efforts are reported weekly by informal means. They have included such results as the following:

1.   The microprogram data entry was begun.

2.   A draft of the working document was completed.

3.   The Block Diagram for Source-Destination block was completed.

4.   The Source-Destination Pascal routines were compiled, coded, tested, and integrated.

5.   Work on the Control block diagrams is being done currently. The control store, micro store, and next address multiplexor were compiled and coded, and the testing is underway.

Efforts Expended

	Man-Hours
Professional Personnel	620
Technical Support Personnel	130

Expenditures

Cumulative expenditures through the month of September 1981 were $92,074. Estimated expenditures during October 1981 were $42,712 leaving an estimated balance of $3,000 from the project ceiling price of $137,786.

Respectfully submitted

W. Dean Spencer
Project Director

APPROVED:

Frederick B. Dyer, Chief
Defense Systems Division